Ar/H2雰囲気における熱処理によるSi(100)表面原子レベル平坦化に関する検討 Atomically flattening process in the Ar/H_2 gas ambient for Si (100) surface

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Introduction



Experimental Procedure



ΤΟΚΥΟ ΤΕΕΗ



[6] S. Kudoh, et al., IEICE Tech. Rep., 115, No.280, SDM2015-72, pp. 7-12 (2015).





The flattening process utilizing sacrificial oxidation and annealing were investigated. ■ Surface RMS roughness was reduced by using wet sacrificial oxidation. ■ Atomically flat surface was obtained by 1050°C/30 min annealing in the Ar/4%H₂ ambient. **Electrical characteristics of MOS diodes on the atomically flat surface were improved dramatically.** The authors would like to thank Prof. Emeritus H. Ishiwara, Prof. T. Sato, Prof. M. Sone, Mr. N. Hatakeyama, and Mr. M. Suzuki of Tokyo Institute of Technology for their support in this research. The authors also would like to thank Prof. Emeritus T. Ohmi, Prof. A. Teramoto, and Prof. R. Kuroda of Tohoku University. Dr. M. Shimada, Mr. M. Hirohara and Mr. I. Tamai of JSW-AFTY for their support and useful discussions for this research.